

S/N 09/145,595

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Ji Ung Lee

Serial No.: 09/145,595

Filed: September 2, 1998

Title: FIELD EMISSION DEVICES HAVING STRUCTURE FOR REDUCED  
EMITTER TIP TO GATE SPACING (as amended herewith)

Examiner: Karabi Guharay

Group Art Unit: 2879

Docket: 303.537US1

**AMENDMENT AND RESPONSE**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Applicant has reviewed the Office Action mailed on April 3, 2000. Please amend the above-identified patent application as follows.

**Affirmation of Election**

As provisionally elected by Applicant's representative, Edward J. Brooks, III, on March 15, 2000, Applicant elects to prosecute the invention of Group II, claims 36-46.

The claims of the non-elected invention, claims 1-35, are hereby canceled. However, Applicant reserves the right to later file continuations or divisions having claims directed to the non-elected inventions.

**IN THE TITLE**

The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed. Therefore please delete the title, "Structure and Method for Reduced Emitter Tip to Gate Spacing in Field Emission Devices" and insert in its place the following title--FIELD EMISSION DEVICES HAVING STRUCTURE FOR REDUCED EMITTER TIP TO GATE SPACING--.

**IN THE CLAIMS**

Please amend the claims as follows:

36. (Once Amended) A field emitter array, comprising:
- a number of cathodes formed in rows along a substrate;
  - a gate insulator formed along the substrate and surrounding the cathodes;
  - a number of gate lines formed on the gate insulator; and

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